

Amendments to the Claims

Please amend claims as follows.

1. (Currently Amended) An apparatus for generating computer assembly code, comprising:
an abstract routine generator for receiving a data stream comprising a multimedia routine and for outputting a generic non-processor-specific abstract representation thereof during runtime; and
a translator for said abstract routine generator for receiving said abstract representation and for outputting processor-specific final code translated from said non-processor-specific abstract representation for processing multimedia input data during said runtime.
- 10 2. (original) The apparatus of Claim 1, wherein said abstract routine generator builds an abstract routine during runtime.
- 15 3. (original) The apparatus of Claim 1, wherein said abstract routine generator builds an abstract routine in the form of a graph.
4. (original) The apparatus of Claim 1 wherein said multimedia data comprise SIMD input data.
- 20 5. (original) The apparatus of Claim 1, wherein said multimedia data comprise image input data.
6. (original) The apparatus of Claim 1, wherein said multimedia data comprise audio input data.
- 25 7. (original) The apparatus of Claim 3, wherein said graph is input to said translator.